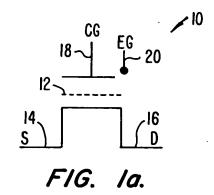
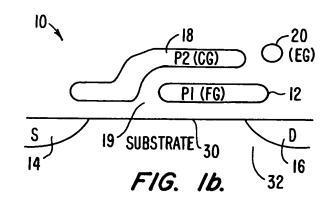
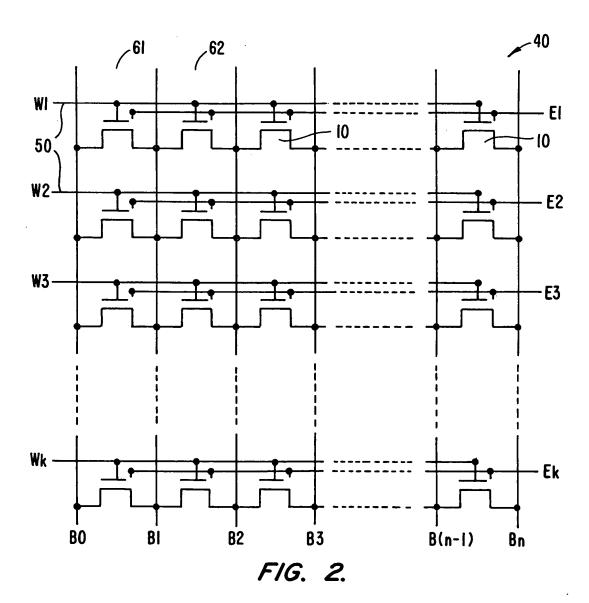


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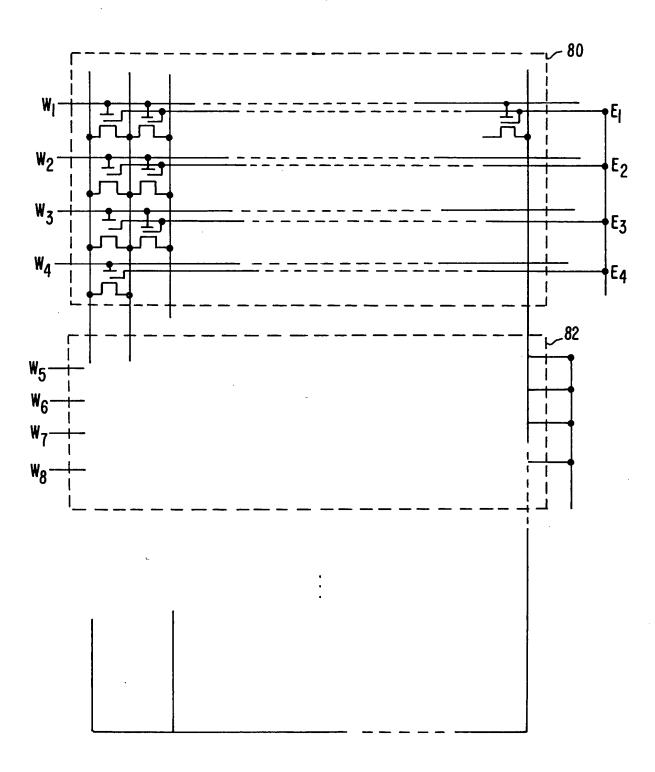


FIG. 2b.

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VOLTAGE CONDITIONS FOR NORMAL MEMORY OPERATIONS

TYPE OF LINES	READ	PROGRAM	ERASE
SELECTED WORD LINE (WL) NON-SELECTED WORD LINE (WL)	5 V 0 V	12V 0V	0 V 0 V
SELECTED SOURCE LINE (SL)	0V	OV	OV
SELECTED DRAIN LINE (DL)	1.57	8V	00
NON-SELECTED BIT LINES (BL) SELECTED ERASE LINE (EL)	FLOAT OV	FLOAT	20V
NON-SELECTED ERASE LINE (EL)	ον	ÖV	OV

FIG. 3.

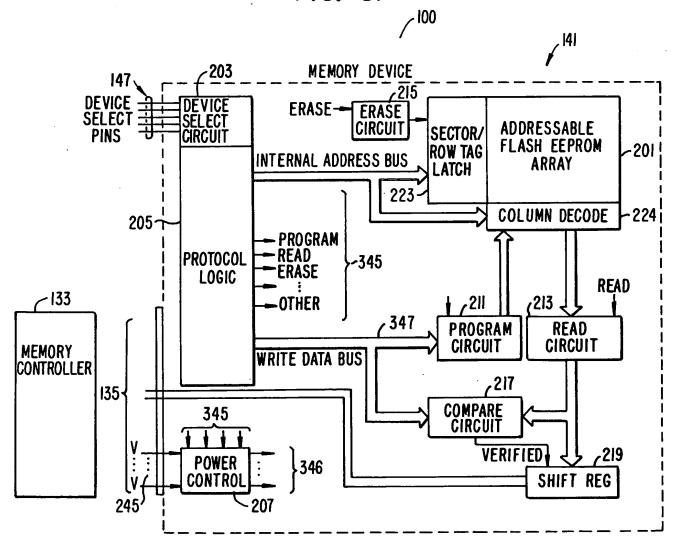
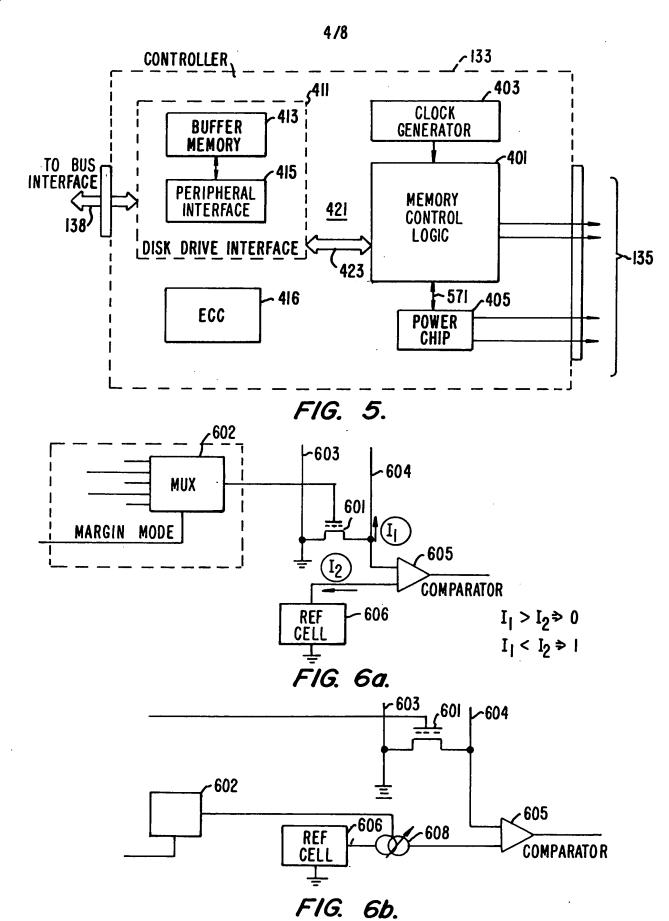
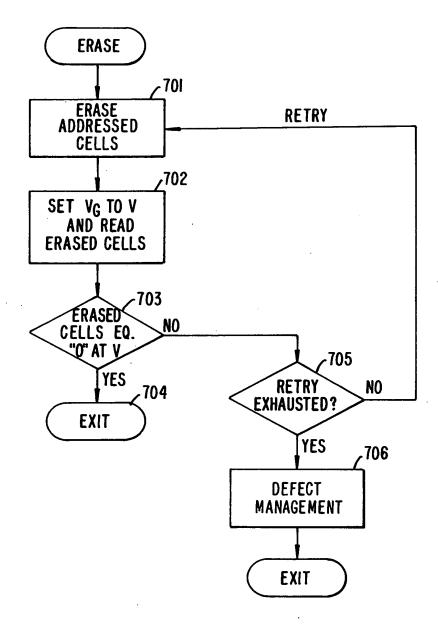


FIG. 4.

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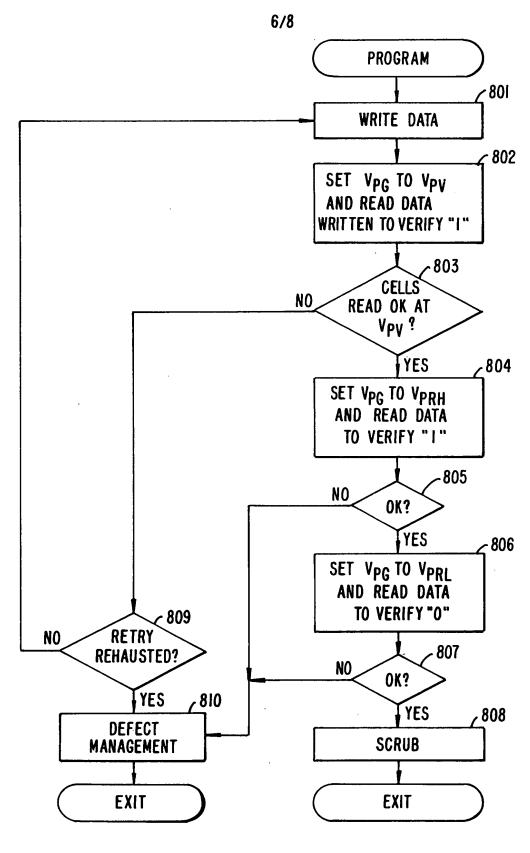


FIG. 8.

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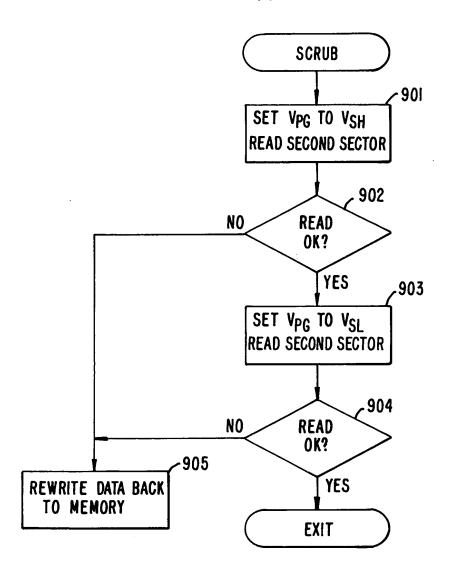
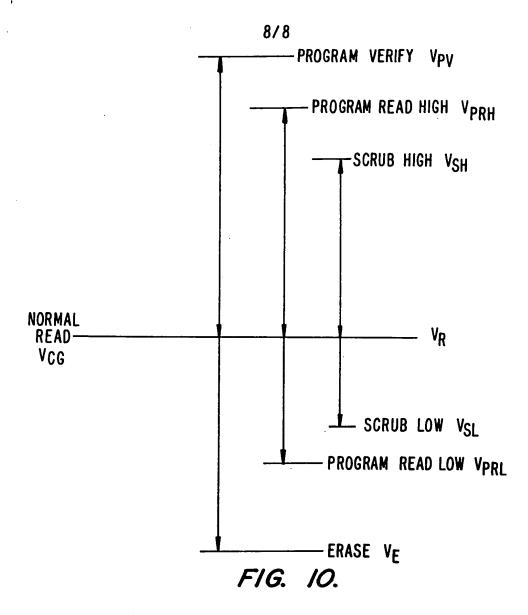


FIG. 9.



٧ _E	3.8
V _{PRL}	4.2
V _{SL}	4.4
v_R	5.0
V _{SH}	5.2
V _{PRH}	5.6
٧N	6.4